What is claimed is:

1.      A method comprising:

receiving an indication of a thermal event in a processor, the processor being part of a

computer system;

in response to the indication, powering down the processor; and

subsequent to the powering down of the processor, powering down other components

of the computer.

2.      The method of claim 1, wherein said other components are located on a

motherboard of the computer system.

3.      The method of claim 1, further comprising:

introducing a predetermined delay after the receiving before said powering down

other components of the computer.

4.      The method of claim 1, wherein said power down other components

comprises:

controlling a state of a signal indicative of a mechanical power switch of the computer

system.

5.      The method of claim 1, wherein said powering down the processor comprises:

cutting off a supply voltage to the processor.

6.      The method of claim 1, wherein said powering down other components

comprises:

cutting off at least one supply voltage to said other components.

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7. A computer system comprising:

a processor capable of indicating a thermal event;

power consuming components;

a power supply subsystem to supply power to the processor and power consuming components; and

a circuit to:

receive an indication of a thermal event in the processor, and

in response to the indication, cause the power supply subsystem to power down the processor before powering down the power consuming components.


8. The computer system of claim 7, wherein said power consuming components are located on a motherboard of the computer system.


9. The computer system of claim 7, wherein the computer system introduces a delay in power down said power consuming components.


10. The computer system of claim 7, further comprising:

a mechanical switch to turn power to the computer system on and off, the computer system having a signal indicative of a state of the switch, wherein

the circuit controls the signal to cause the power down of said power consuming components.


11. The computer system of claim 7, wherein the power supply subsystem powers down the processor by cutting off a supply voltage to the processor.


12. The computer system of claim 7, wherein the power supply subsystem powers down the power consuming components by cutting off at least one supply voltage to said other components.

13. A method comprising:

receiving an indication of a thermal event in a processor, the processor being part of a computer system;

in response to the indication, introducing a delay;

in response to the indication, powering down the processor before the expiration of the delay; and

powering down powering down other components of the computer in response to the expiration of the delay.

14. The method of claim 13, wherein said other components are located on a motherboard of the computer system.

15. The method of claim 13, wherein said powering down other components comprises:

controlling a state of a signal indicative of a mechanical power switch of the computer system.

16. The method of claim 13, wherein said powering down the processor comprises:

cutting off a supply voltage to the processor.

17. The method of claim 13, wherein said powering down other components comprises:

cutting off at least one supply voltage to said other components.